

Title: Autotransformer-Based System and Method
of Current Harmonics Reduction in a Circuit
Inventor(s): Girish R. Kamath
Appln. No. Not yet assigned
Docket # 37125/31374

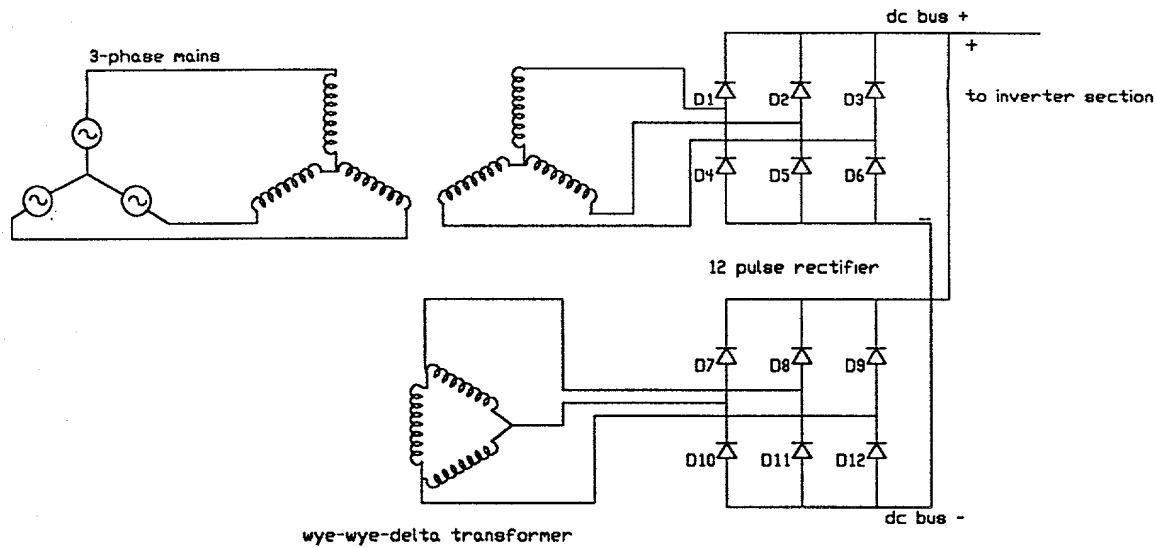


Fig. 1
Prior Art

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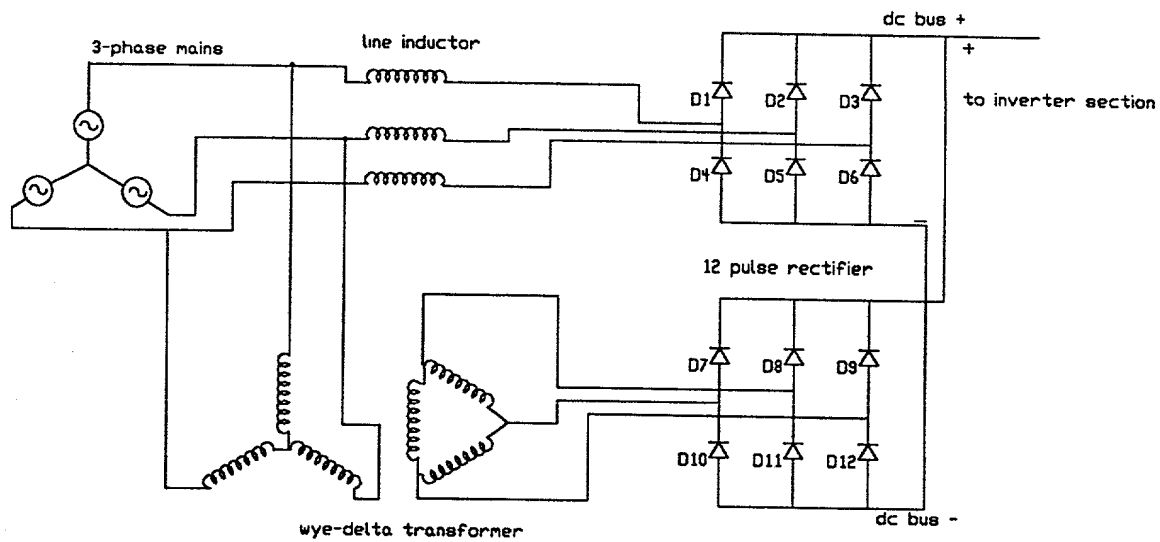


Fig. 2
Prior Art

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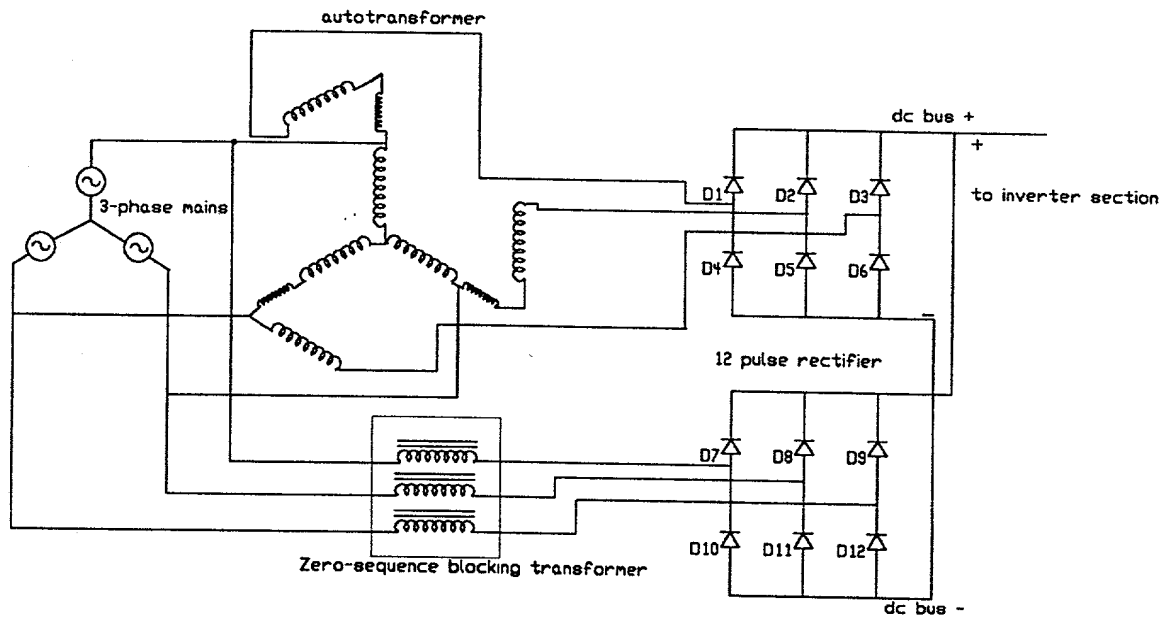


Fig. 3
Prior Art

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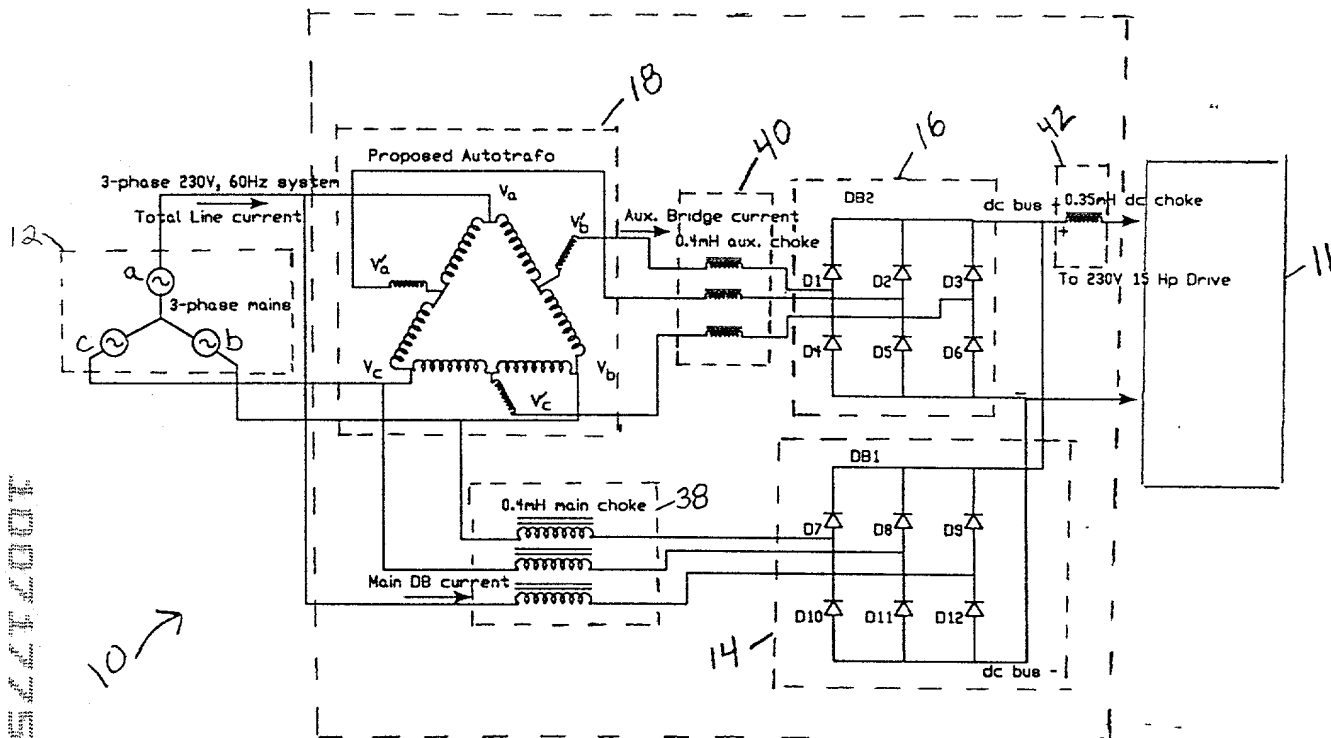


Fig. 4

10071775 020702

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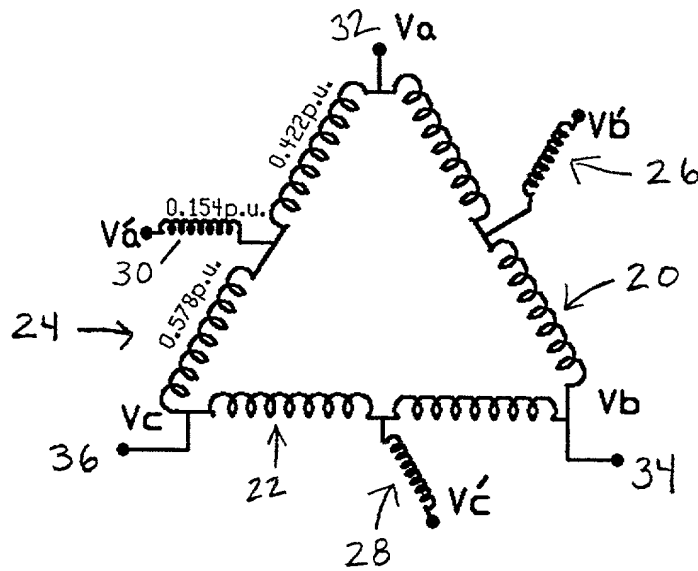


Fig. 5

10074775.020702

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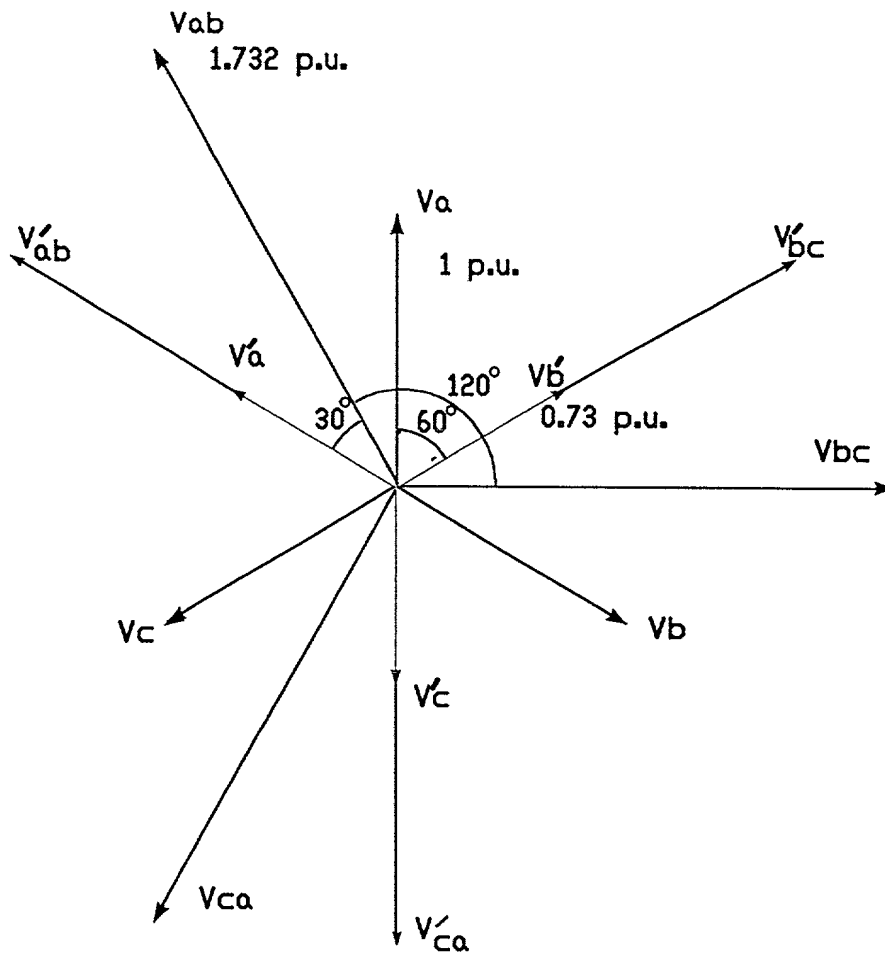


Fig. 6

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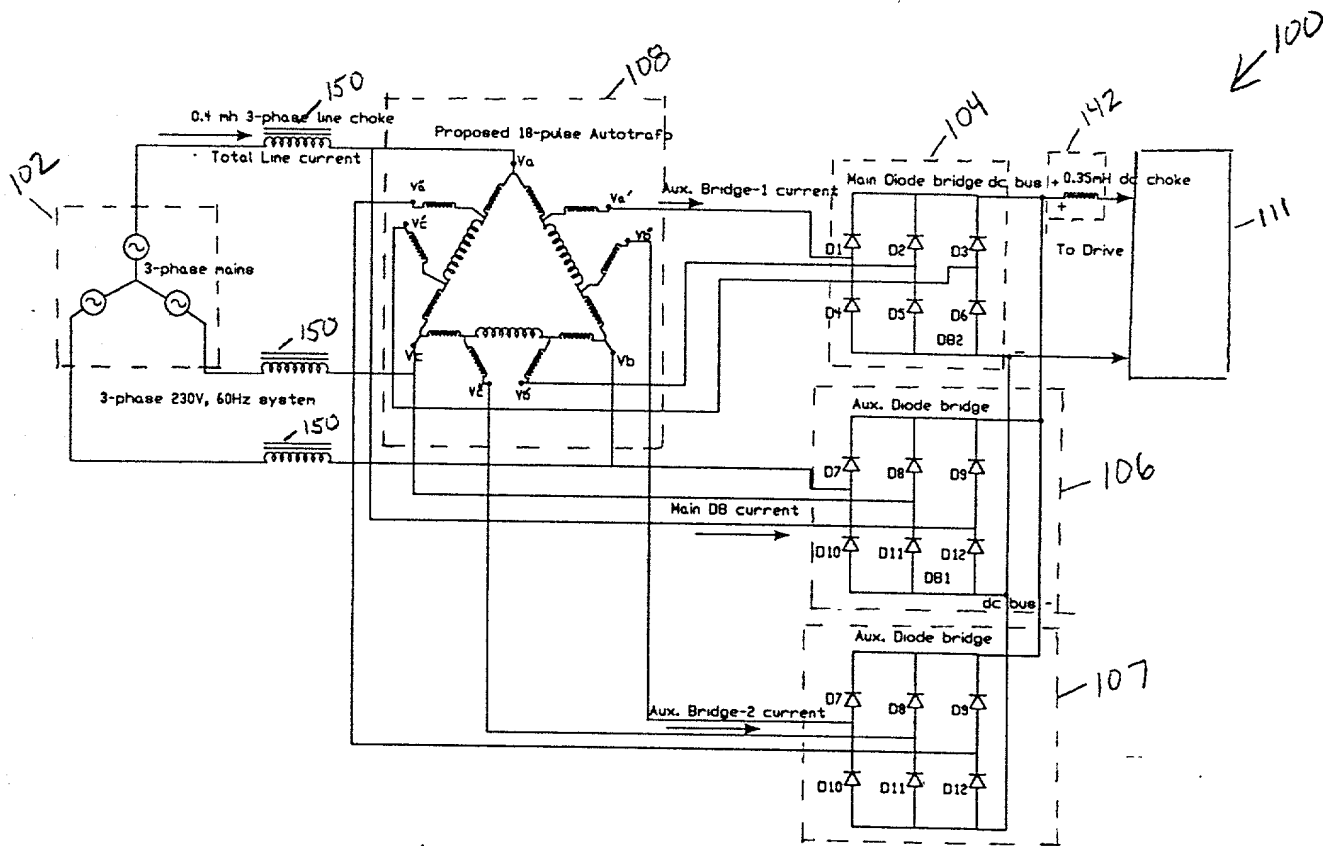


Fig. 7

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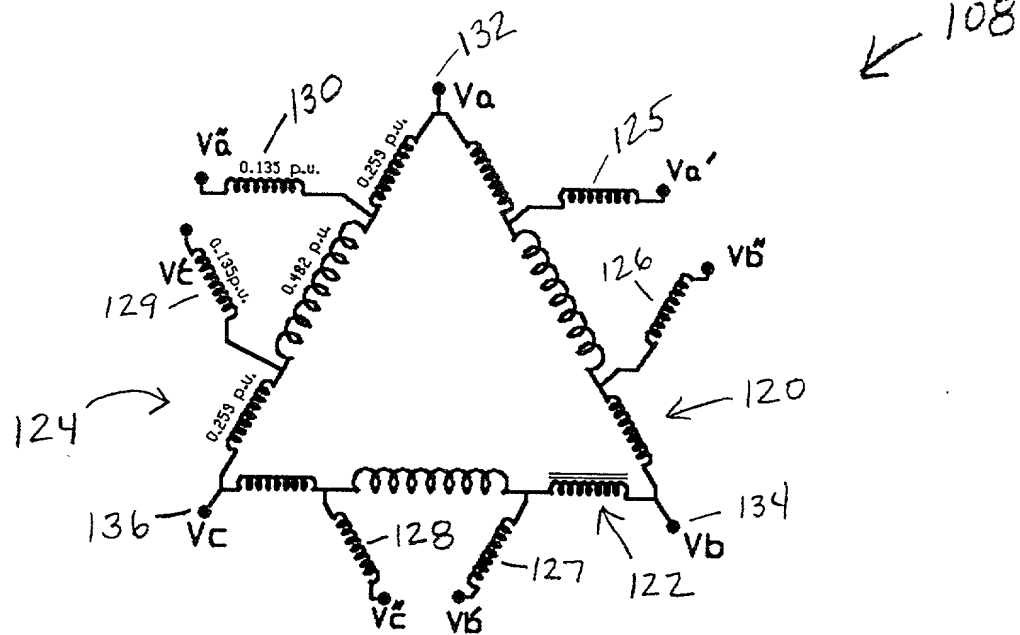


Fig. 8

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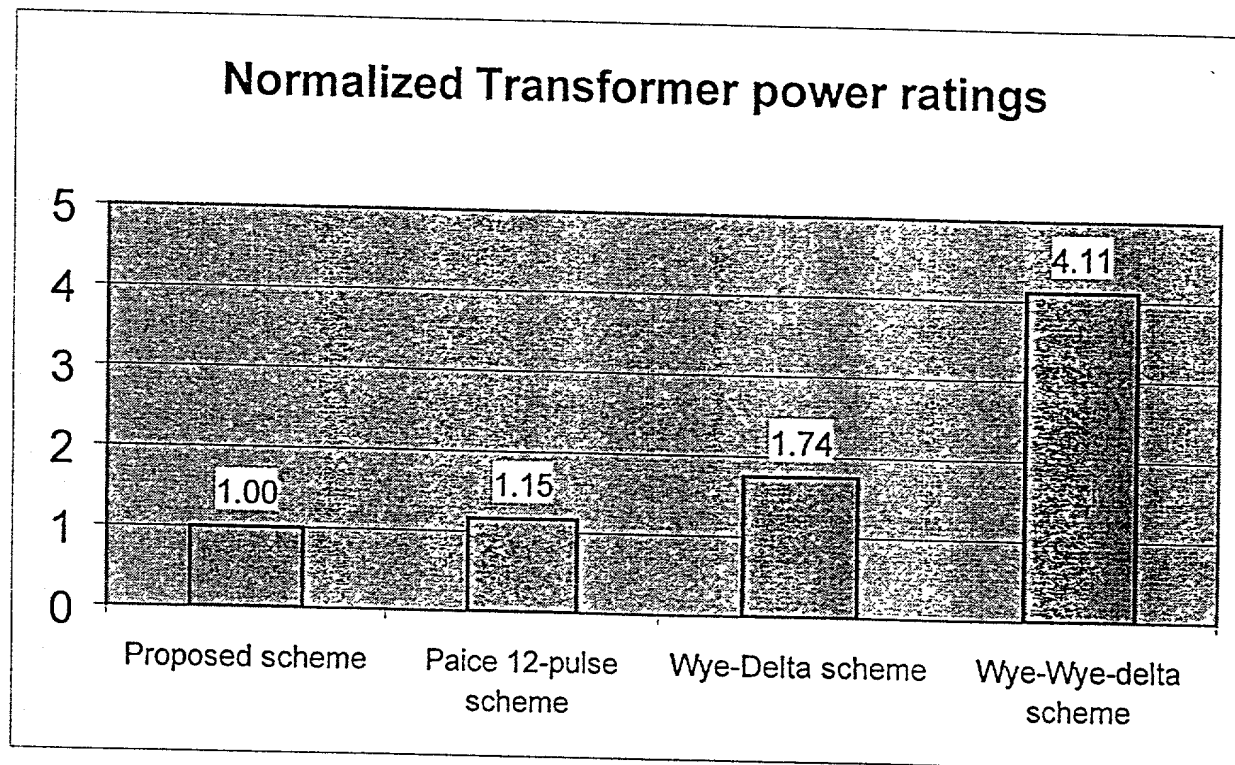


Fig. 9

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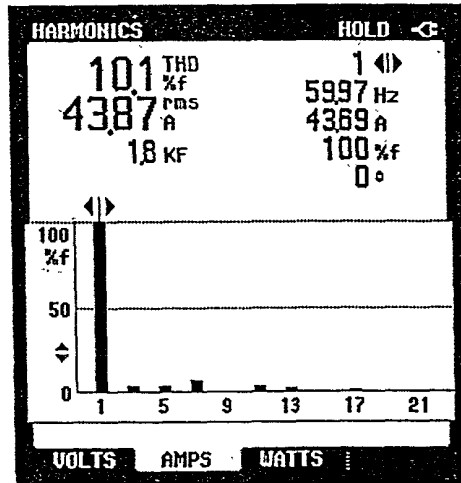


Fig. 10

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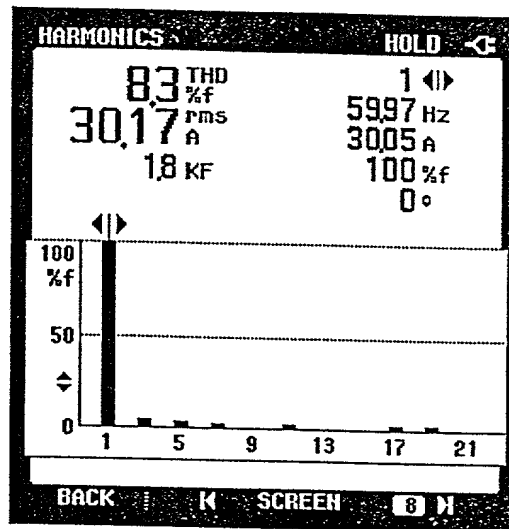


Fig. 11A

100175-102070

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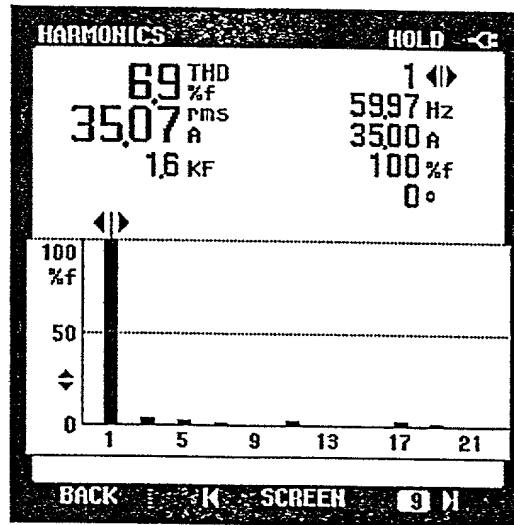


Fig. 11B

1007175-020702